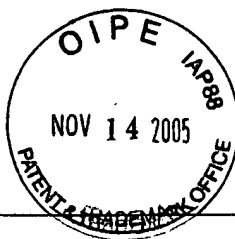


AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116
Serial Number: 09/930,102
Filing Date: August 14, 2001
Title: Clock signal decoupling for synchronous operation
Assignee: Intel Corporation



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Dkt: P18420 (INTEL)

S/N 09/930,102

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Ang et al.	Examiner:	Habte Mered
Serial No.:	09/930,102	Group Art Unit:	2662
Filed:	August 14, 2001	Docket No.:	P18420
Title:	CLOCK SIGNAL DECOUPLING FOR SYNCHRONOUS OPERATION		
Assignee:	Intel Corporation	Customer No:	21186

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant has reviewed the Final Office Action mailed on September 9, 2005. Please amend the above-identified patent application as follows.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks / Arguments begin on page 9 of this paper.